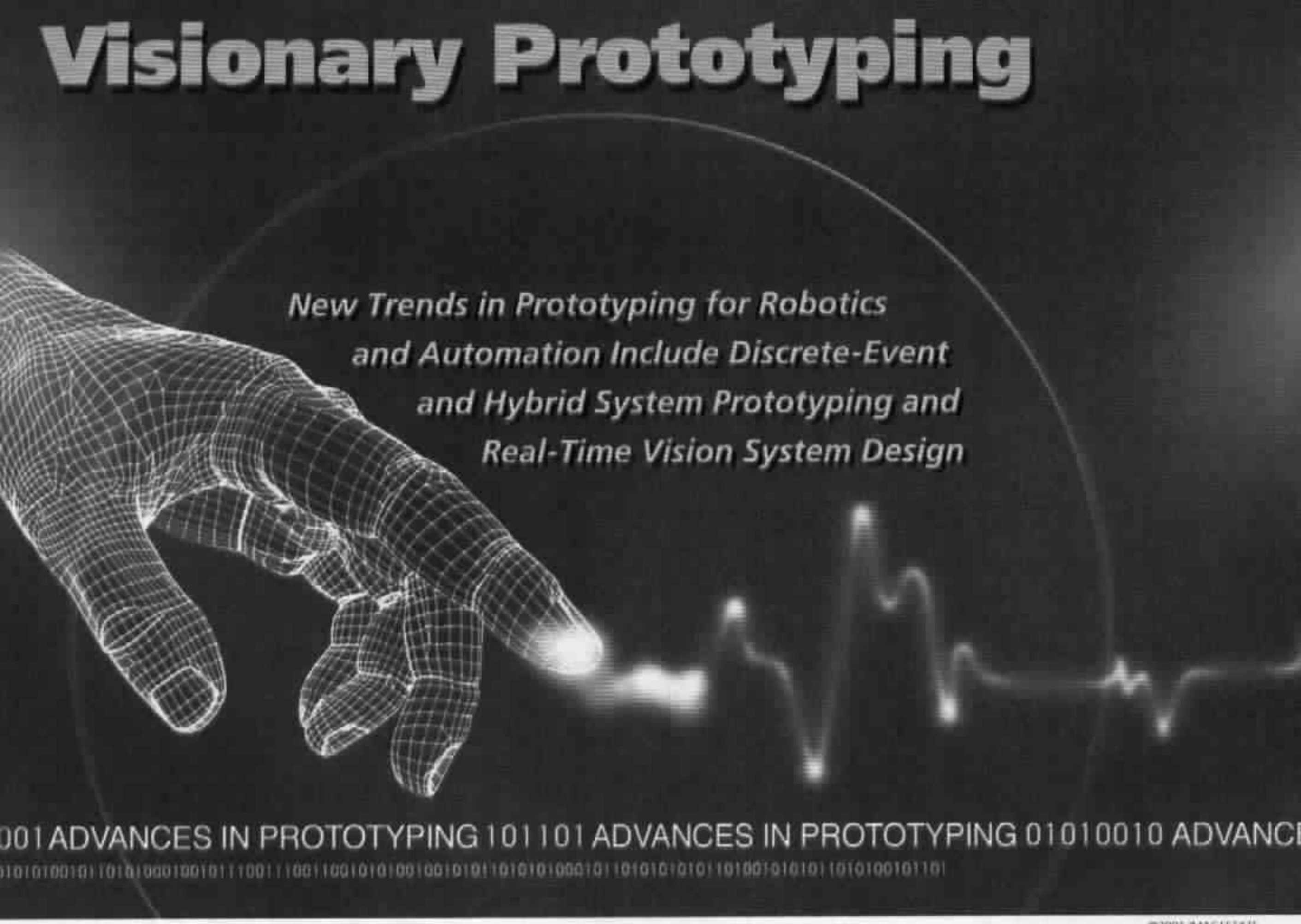
Visionary Prototyping

New Trends in Prototyping for Robotics and Automation Include Discrete-Event and Hybrid System Prototyping and Real-Time Vision System Design



by KOK-MENG LEE and TAREK M. SOBH

During the last two decades, rapid advancements in computer, communication, and control technologies have greatly accelerated the efforts of developing novel prototypes and their cost-effective applications in automation. Today, besides introducing intelligence directly into equipment/systems through embedded microcomputers and providing virtual prototyping through enhanced computer-aided (CAD)/ computer-aided engineering (CAE) facilities, information flow is well regarded as an essential part of the integrated design approach whereby all members of the prototype development and manufacturing automation team can work closely together throughout the design and manufacturing cycle.

This article focuses on two subtopics. The first is the development of a theory for prototyping discrete-event and hybrid systems and its applications. In discrete-event dynamic systems (DEDS), state transitions are caused by internal, discrete events in the system. DEDS are attracting considerable interest, and current applications are found in manufacturing systems, communications and air traffic systems, robotics, autonomous systems, and artificial intelligence. An overview for the development of a simple graphical environment for simulating, analyzing, synthesizing, monitoring, and controlling discrete-event and hybrid systems is also presented. The second focus is on prototyping machine vision for real-time automation applications. We discuss the problems associated with traditional machine vision systems for cost-effective, real-time applications, novel alternative system design to overcome these problems, and the new trends of modern vision sensors. Modern smart sensors provide the features of traditional machine vision systems at less than half of the usual price by eliminating the signal-conversion electronics, fixed-frame rates, and limited gray-scale quantization. The camera, image-acquisition electronics, and computer are integrated into a single unit to allow dynamic access to the charge-coupled devices (CCDs) without image float or flutter. We also present a physically accurate image synthesis method as a flexible, practical tool for examining a large number of hardware/software configuration combinations for a wide range of parts.

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Discrete-Event and Hybrid Systems

The underlying mathematical representation of complex computer-controlled systems is still insufficient to create a set of models that accurately capture the dynamics of the systems over the entire range of system operation. We remain in a situation where we must trade off the accuracy of our models with the manageability of the models. Closed-form solutions of mathematical models are almost exclusively limited to linear system models. Computer simulation of nonlinear and discrete-event models provides a means for offline design of control systems. Guarantees of system performance are limited to those regions where robustness conditions apply. These conditions may not apply during startup and shutdown or during periods of anomalous operation.

Rapid advancements of computer, communication, and control technologies have accelerated the efforts of developing novel prototypes and their cost-effective applications in automation.

Recently, attempts have been made to model low- and high-level system changes in automated and semi-automatic systems as DEDS. Several attempts to improve the modeling capabilities are focused on mapping the continuous world into a discrete one. However, repeated results are available that indicate that large interactive systems evolve into states where minor events can lead to a catastrophe. Discrete-event systems (DES) have been used in many domains to model and control system state changes within a process. Some of the domains include

- manufacturing;
- robotics;
- autonomous agent modeling;
- control theory;
- assembly and planning;
- concurrency control;
- distributed systems;
- hierarchical control;
- highway-traffic control;
- autonomous observation under uncertainty;
- operating systems;
- communication protocols;
- real-time systems;
- scheduling; and
- simulation.

A number of tools and modeling techniques are being used to model and control discrete-event systems in the above domains. Some of the modeling strategies include

- timed, untimed and stochastic Petri nets (PNs) and state automata;
- Markovian, stochastic, and perturbation models;
- state machines;
- hierarchical state machines;
- hybrid systems modeling;
- probabilistic modeling (uncertainty recovery and representation);
- queuing theory; and
- recursive functions.

Here, we present a brief review for prototyping discrete-event and hybrid systems, discuss some techniques used in the DEDS field, and present a simple software prototyping tool for representing hybrid DES.

Hybrid and Discrete-Event Systems

DEDS are dynamic systems in which state transitions are triggered by the occurrence of discrete-events in the system. DEDS are suitable for representing hybrid systems, which contain one or more of the following characteristics:

- continuous domain,
- discrete domain,
- chaotic behavior, and
- symbolic parameters.

Some examples of DEDS are

- Data Network: A = {send, receive, timeout, lost}
- Shop with k jobs: A = {admit_job, job_finished}
- Electric Distribution: A = {normal, short_circuit, over_current}.

There are several frameworks that can be used to model DEDS such as: finite automata, PNs, Markov chains, etc. Choosing one of these frameworks depends on the nature of the problem being modeled and the implementation techniques available to implement this model.

DEDS have been applied to model many real-time problems and are involved in different types of applications. Some of these applications are

- networks,
- manufacturing (sensing, inspection, and assembly),
- economy,
- robotics (cooperating agents),
- highway traffic control, and
- operating systems.

For more details about DEDS applications see [1]-[6]. DEDS will have an important role in the development and improvement of many other applications in different disciplines.

Discrete-Event Models

As mentioned before, there are several representations and frameworks used in DEDS modeling. Some of these frameworks are

- automata (untimed, timed, temporal, stochastic);
- pushdown automata, μ-recursive, and turing machines;
- PNs (timed, untimed);
- Markov chains;
- queuing theory;

- min-max algebra;
- uncertainty modeling; and
- classical control.

These frameworks can be categorized in three different domains:

- *Timed versus untimed models:* the untimed models emphasize the "state-event sequence" of a DEDS and ignore the holding time of each state, while in the timed models, "time" is an essential part of the model.
- *Deterministic versus probabilistic models:* deterministic models assume preknowledge of the sequence of events that will occur at any time, while probabilistic (stochastic) models associate probabilities with each event.
- *Computational models:* these can be
  - *logical models* in which the primary questions are of qualitative or logical nature;
  - *algebraic models,* which can capture the description of the trajectories in terms of a finite set of algebraic operations; and
  - *performance models,* which form in terms of continuous variables, such as average throughput, waiting time, etc.

Fig. 1 shows the different models of representing DEDS and their characteristics. More about DEDS models can be found in [3].

Evaluation of DEDS

The evaluation of each framework can be done in four dimensions:

- Descriptive power
  - Language complexity is based on the formal theory of languages. Each finite-state machine (FSM) generates a language L that represents all possible traces of this FSM:

$$L(\text{FSM}) \subset L(\text{PNs}).$$

So, PNs are more *language complex* than FSM.

  - Algebraic complexity is based on systems theory. We can consider any algebraic system as a set of models and a set of operators that map one or more models to another. For example, in transfer functions, addition and multiplication reflect serial and parallel systems.
- Implementation
- Performance evaluation
  - Logical correctness is a desirable property of the traces generated by any DEDS model. In the data network example, we must guarantee that each transmitted packet has been received correctly by the receiver.
  - Real-time requirement is a desirable property of the real-time response of the actual system. It is necessary to embed the DEDS model in a real-time environment.
- Applications.

A Simple Prototyping Tool

We have built a software environment to aid in the design, analysis, and simulation of discrete-event and hybrid systems. The environment allows the user to build a system using either FSMs or PNs. The environment runs under X/Motif and supports a graphical DES hybrid controller, simulator, and analysis framework. The framework allows for the control, simulation, and monitoring of dynamic systems that exhibit a combination of symbolic, continuous, discrete, and chaotic behaviors, and includes stochastic timing descriptions (for events, states, and computation time); probabilistic transitions; controllability and observability definitions; temporal, timed, state space, PNs, and recursive representations; analysis; and synthesis algorithms.

The environment allows not only the graphical construction and mathematical analysis of various timing paths and control structures but also produces C code to be used as a controller for the system under consideration.

Using the environment is fairly simple. For FSMs the designer uses the mouse to place states (represented by ovals) and connect them with events (represented by arrows). Transitions and states can be added, moved, and deleted easily. Fig. 2 is an example of a simple stochastically timed FSM, containing four states and five events.

The probabilities on the events (that is, which path to navigate in the automaton) are designated using the marked field in the status dialog box. The different timings (on event and state times) and distribution function type, mean, and variance can be assigned through the status dialog box too. The allow-

	Timed	Untimed
Logical	Temporal Logic Timed Petri Nets	Finite State Machines Petri Nets
Algebraic	Min-Max Algebra	Finitely Recursive Proc. Comm. Sequential Proc.
Performance	Markov Chains Queuing Networks GSMP/Simulation Stochastic Petri Nets	
	Stochastic →	← Nonstochastic

Figure 1. *Different models for DEDS.*

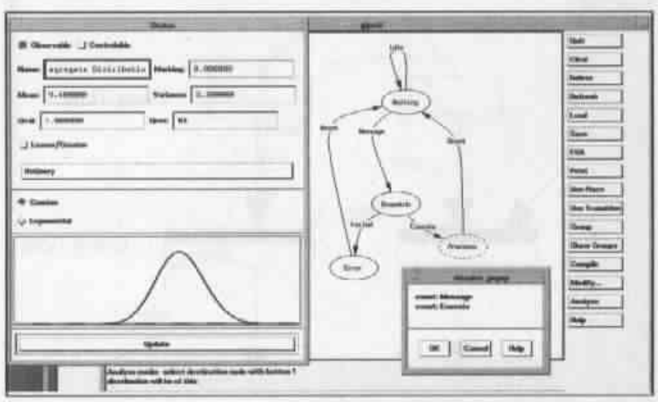

Figure 2. *A stochastically timed FSM window during analysis.*

able distributions are currently restricted to Gaussian and exponential functions, but they can be easily extended to arbitrary discrete or continuous distributions. A window shows the distribution function at a state or event and allows the user to do queries; for example, queries on whether a path time probability is greater or less than a given time, combined timing distributions to reach a goal state through various paths, etc. The dialog box allows the user to perform queries of various kinds. The currently selected state/event is drawn with a dashed line, and the information in the status window pertains to it. Optimizing paths based on stochastic timing can also be performed; in that case, windows will pop out with the event path and the status window will have the combined distribution function. Fig. 3 presents an automaton model in the environment. The environment also produces C code for controlling the system under consideration. In our PN model, we have extended the definition of stochastic timed PNs to have additional timings. Our model has three times associated with it: place, delay, and

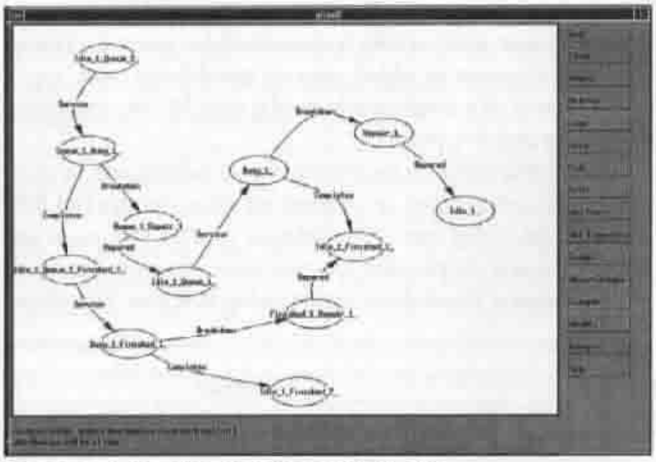

Figure 3. A snapshot of the FSM environment.

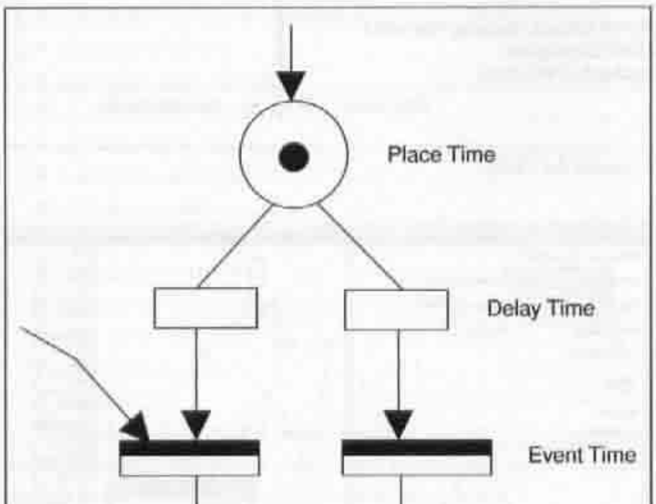

Figure 4. The proposed three time zones for a timed PN.

event (see Fig. 4). The place time is where the token is held back and delays the enabling of the transition. This represents the computation time of that place. The delay time is associated with the input arcs to a transition. It represents the time to leave the corresponding place. The event time is analogous to the single time in stochastic timed PNs, which is called *firing time*. We believe that this lends to a more intuitive representation of the times and simplifies the modeling task since it captures more details than the original timed PN model.

We can define the new model as:

$$PN = (P, T, A, W, x_0)$$

where

- P = set of places with associated random variables;
- T = set of transitions;
- $A = A_{in} \cup A_{out}$ with
  - A_{in} set of elements from $\{P \times T\}$ with associated random variables,
  - A_{out} set of elements from $\{T \times P\}$;
- W = a weight function, $w: A \rightarrow \{1,2,3,\ldots\}$; and
- x_0 is an initial marking.

The environment for PNs is similar. Circles, transitions by ellipses, and arcs by arrows represent places graphically. As mentioned above, there are three locations where one can place timing information: event time, which is the time the actual event takes; place time, when a token is moved through a transition firing there is a place time that hides the token until it has expired; and delay time, which comes into effect when a transition fires—it is the time for the event to reach the transition. The event time will not start until all of its input tokens delay time has expired. Fig. 5 depicts a snapshot of the PN environment in action.

The system generates C code for the user hybrid system, so one can simulate and control an actual system using the code. The C code is currently generated for FSMs (soon code will be generated for PNs too). A PN will be converted to an FSM before code is generated; all of the timing is then placed on the events. The user has to select the initial state and provide the function for simulating/generating the events; the code will keep track of the elapsed simulated time and will return when it reaches a state with no transitions.

The environment allows conversion back and forth between the FSM and PN models. Conversion to a PN is straightforward, but one loses the event probabilities. The only thing that is needed is to create a transition for every event. Conversion from a PN to an FSM is only possible if the PN is *k*-bounded, which means no place can ever have more than *k* tokens. The system generates a state for every possible marking of that net. The states are represented as the marking; the events are just the transitions. The three "times" are pushed into the events. The system convolves the maximum of the input delays with the event and the maximum of the place times. The maximum function is a standard convolution, except that the maximum is used instead of multiplication.

The algorithm for generating all of the markings starts with some initial marking and then goes through all of the possible transitions. If it can fire, the firing is simulated, and the new marking is inserted into the set of states. If it is already represented, the transition is kept. Otherwise, the transition is kept and recursion is done with the new marking. This process is repeated until no transitions can be fired.

Our system can serve as a simple graphical simulator, analyzer, synthesizer, monitor, and controller for hybrid systems models using either PN or FSM high-level frameworks.

Trends in Machine Vision System Design

The predictive model provides a baseline of "in-advance" information, but if routine deviations are greater than can be tolerated, sensors are needed to augment this baseline information for feedback to controllers. In existing systems, estimates of the impact of sensing systems on process performance indicate as much as a six-fold increase in effective operation speed [7]. A general review of different sensors for robotics and automation can be found in [8]. One of the major contributions of information technologies to sensors was the idea of digitized output, which removed analog variation from the outputs. A good illustrative example is machine vision, which grows from a standard composite video signal that the television industry uses, to a more general-purpose sensor with onboard intelligence.

However, although it has been well recognized in the past three decades that vision can add considerably to flexibility (by simplifying grippers, component feeders, and location tooling) and can reduce the engineering time required to implement it, the capabilities of commercial vision systems for use in part verification, kitting, and presentation for robotic assembly are still very limited. Until the late 1980s, most vision systems employed a camera that output a video signal limited by the traditional TV standard (typically 30 f/s specified by the RS170 established in the 1950s) and an object-dependent structured illumination. For use as a robot vision system, a frame grabber board and a high-performance host computer must accompany the video camera. The conventional vision approach generally discards color information and requires a substantial amount of memory and data communication time as well as sophisticated vision interpretation. Variations in surface reflectance, coupled with algorithm computational demands, often make the conventional approach too expensive, unreliable, and slow. In addition, the conventional vision approach, which attempts to emulate human eyes and brain, does not necessarily yield the accurate data required by the robots.

Alternative Vision System Architecture

To overcome the problems associated with the traditional video-based vision system, several vision systems were designed for robotic applications. Among these is a flexible integrated vision system (FIVS) developed at Georgia Tech in the late 1980s [9], which offers performance and cost advantages by integrating the imaging sensor, control, illumination, direct digitization, computation, and data communication in a single unit. By eliminating the host computer and frame grabber, the camera is no longer restricted by the RS-170 standard and, thus, frame rates higher than 30 fps can be achieved.

DEDS are attracting interest, and current applications are found in manufacturing systems, communications and air-traffic systems, robotics, autonomous systems, and artificial intelligence.

ARCHITECTURE

As shown in Fig. 6, the central control unit of the flexible integrated vision system is a microprocessor-based control board. The design is to have all of the real-time processing performed using the microprocessor control board without relying on any other system or computer. The prototype of FIVS is shown in Fig. 7.

ONBOARD PROCESSOR

The digital signal processor (DSP)-based control board is designed to communicate with several option boards in parallel to tailor the system for a number of applications. Each of these option boards is controlled independently by a programmable logic device (PLD), which receives a peripheral select signal, a read/write signal, and an address signal from the microprocessor control board. Typical examples of the option boards for the FIVS are the digi-

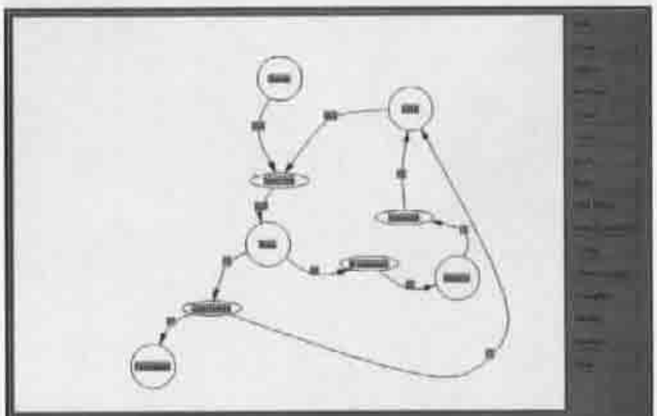

Figure 5. A snapshot of the PN environment.

taf-video head, a real-time video record/display/playback board, and an expandable memory board.

CAMERA

The video head consists of an $m \times n$ CCD array, the output of which is conditioned by high-bandwidth amplification circuitry. The output is then sampled by a "flash" analog-to-digital converter (ADC). The DSP-based control board provides direct software control of the CCD array scanning and integration time, the intensity of the collocated illumination, and the real-time execution of a user-selectable vision algorithm imbedded in the electrically erasable/programable read-only memory (EEPROM). In operation, the PLD decodes the control signals to initiate row and column shifts in response to commands from the DSP-based control board. Particular row and column shifts enable the retrieval of only a specific relevant area from an image. The PLD also provides control signals to the ADC for performing the analog-to-digital conversion synchronized with row shifts and enables the video buffer when the DSP reads or writes data to the video random access memory (VRAM).

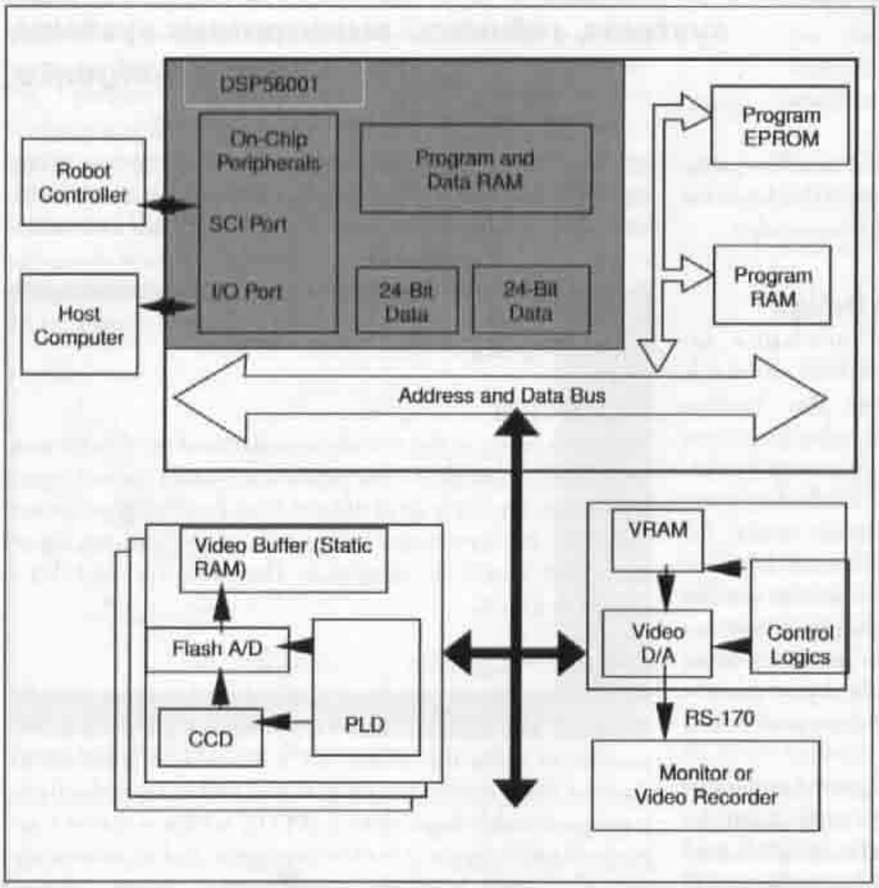

Figure 6. *Schematic of a flexible integrated vision system.*

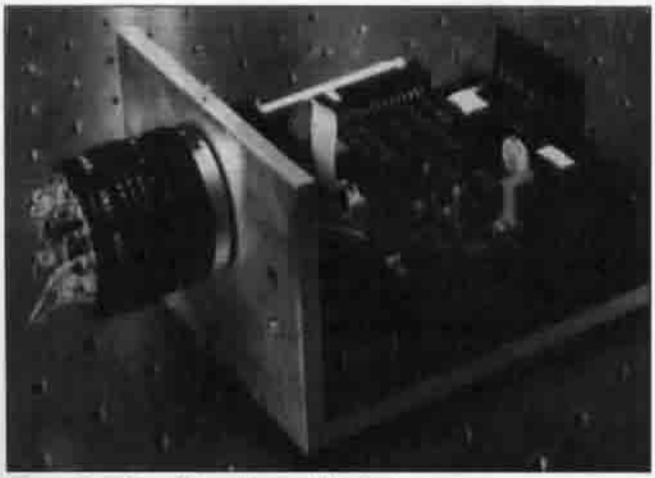

Figure 7. *FIVS and its collocated illumination system.*

IMBEDDED SOFTWARE

The vision-system imbedded software gives users the flexibility to control the CCD array scanning and integration time and the intensity of the illumination. With the CCD under software control, partial frames can be captured instead of the customary full frame, reducing the cycle time required to capture and process an image. The ability to shift out partial frames is ideal for high-speed tracking applications where the approximate location is known from a prior image. By reducing the time to capture an image, the effective frame rate is increased. For example, shifting out 1/4 of an image can increase the frame rate up to 480 fps, not including the time required for illumination and image processing. This frame rate is 16 times the rate achievable from the RS-170 standard.

The New Trends

Unlike conventional RS170-based systems, which require pixel data to be stored in a video buffer before processing of pixel data can commence, the FIVS design provides an option to completely bypass the video buffer and thus offers a means to process and/or store the digitized pixel data by directly transferring the ADC output to the DSP. For real-time vision-based object tracking and motion control system applications, the scheme represents a significant saving in time and video buffer size required for processing an image. As an illustration, consider an image array of $m \times n$ pixels. The time needed to store the entire image (with no computation) in a memory at K MHz is $(m \times n)/K$ µs and requires $m \times n$ bytes of memory. Typical array size of a CCD ranges from 200×160 to 4096×4096 of pixels. The corresponding video buffer and time required simply to store the entire image at a clock rate of 10 MHz would range from 32 kB to 16 MB and 3.2 ms to 1600 ms, respectively! Clearly, the option to completely bypass the video buffer offers a potentially useful solution to eliminate the frame storage prerequisite that is often required in conventional vision systems. Furthermore, this scheme completely eliminates the special hardware needed in acquiring the digitized pixel data for storage.

APPLICATIONS

With onboard intelligence, computer-controlled machine vision systems have found a number of real-time applications where the accuracy of image gray-scale pixel values far outweighs image. Some of these examples are robotic part pickup [10], motion tracking [11], three degree-of-freedom (DOF) orientation sensing [12], servo-track-writing in hard disk drive manufacturing [13], disassembly automation [14], and haptic sensors [15].

Modern smart sensors provide the features of a traditional machine vision systems by eliminating the signal-conversion electronics, fixed-frame rates, and limited gray-scale quantization.

The advances in direct-digital machine vision will continue to lead to new ways of addressing industrial automation problems that were difficult, if not impossible, to solve, particularly for traditional industries. One such example is an ongoing development of a high-speed, live-bird-handling system for poultry processing applications, where machine vision has played a significant role in automating the process of transferring live birds from a moving conveyor to shackles, typically at a line speed of 3 birds/s. Live-bird-handling problems have been found difficult because the birds tend to flail about when they are caught. Nonevasive techniques must be developed along with the study of stimulus environments to promote behavior compliance, the study of the role of visual responsiveness, and the evaluation of vision acuity in different spectral environments. Often such real-time control application requires a stringent combination of structured illumination, reflectance, and imaging sensor.

Structured illumination and reflectance of a machine vision system could play a significant role in the live-bird-handling application [16] where the bird's posture is determined for real-time manipulation of the bird's legs. In order to keep the bird from flailing and its presentation uniform as the bird enters the a grasper, retroreflective sensing technique [10] has been used to obtain a snapshot of the moving bird. The structured illumination system consists of a low-intensity spectrally filtered illumination and a vision system that captures a snapshot of the bird against a retroreflective background. Fig. 8 shows an image of a bird on a conveyor moving at 0.5 m/s toward the grasper. The image of the bird was captured against a 580-85 Black Scotchlite retroreflective background with a low-intensity illumination filtered with a Roscolux full-blue filter, since birds are insensitive to blue light (or low 400 nm wavelengths).

ADVANCES IN NEW IMAGING SENSORS

In the early 1990s, complementary metal-oxide semiconductor (CMOS) sensors emerged as low-cost, low-power alternatives to CCD. The principle architectural solutions, which enabled high-data throughput, are the effective integration of pixel readout processing, ADC conversion, and the high-speed dual-port RAM in one single chip. The core of the CMOS sensor designs is an $m \times n$ photodiode active pixel array, which is accessed in row-wise fashion and read out into column ADCs in parallel. With the addition of a column of dual-port static random access memory (SRAM), the readout of the digital data can be done during the analog-to-digital (A/D) conversion of the next row. The sensor has an onchip digital block that runs the row processing, ADC conversion, and readout; allows flexibility in selecting rows and columns; and defines the start time for row processing or read. The use of parallel pixel readout and digitizing, as well as easy ways of multiplexing/de-multiplexing data—techniques required for high-speed large format sensors—is challenging CCD technology in mainstream applications. Today, CMOS sensors (for example, the 1024 × 1024 CMOS active pixel sensor [17]) have the potential to achieve very high-output data rates over 500 MB/s and a low-power dissipation of 350 MW at a clock rate of 66 MHz.

Attempts to emulate human visual perception have led to the development of high dynamic-range color (HDRC) imaging systems [18]. The power of human visual perception lies in its very high dynamic range and its robust object detection due to high and constant contrast resolution in both bright and dark regions of a scene. Natural photoreceptors, like those in our eyes, have a logarithmic response that detects very fine absolute steps in the dark or shade, while they limit their response to larger absolute steps at high intensities. HDRC CMOS pixels generate an output voltage equivalent to the log

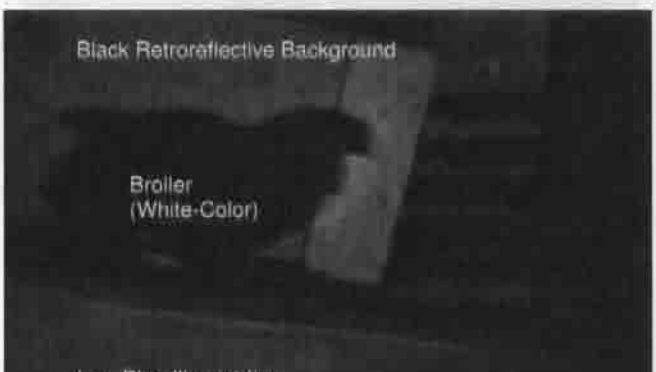

Figure 8. An example with structured illumination/reflectance (snapshot of a live bird).

of the local optical intensity. As a result, the high dynamic-range of the HDRC camera outperforms the digital CCD camera that has reached its limits, in spite of its advantage in resolution and all its postprocessing power.

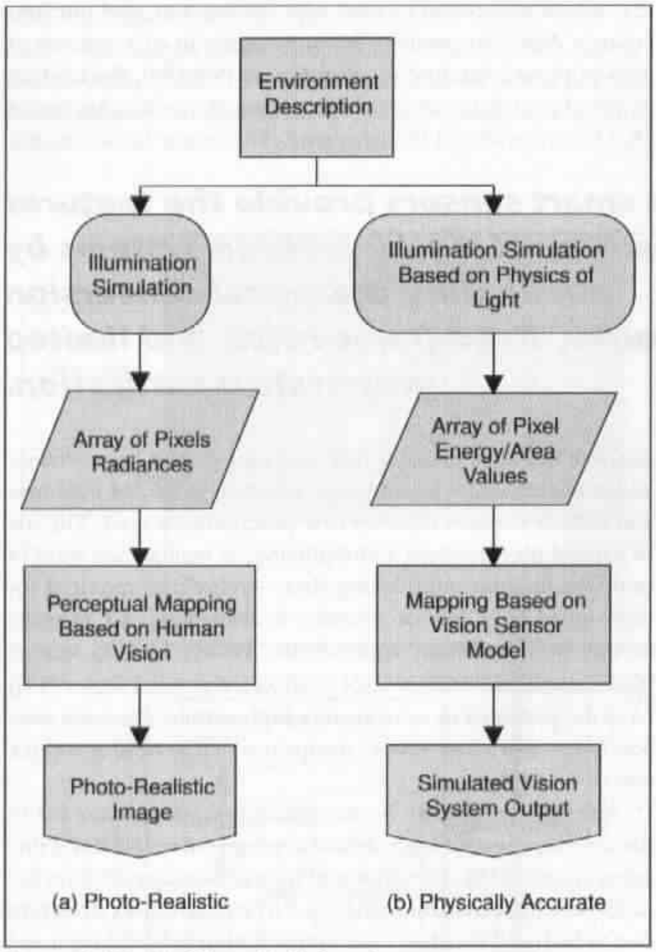

Figure 9. *Model of the synthetic-imaging process.*

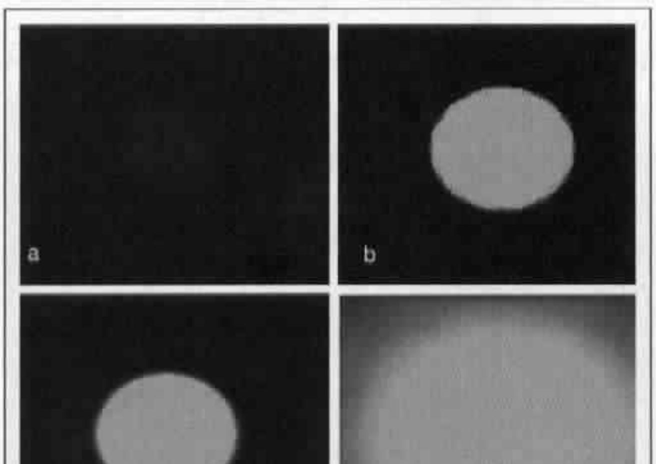

Figure 10. *Synthetic images of retroreflective field.*

COMPUTING AND INTEGRATION

The recent introduction of microprocessors with large internal caches and high-performance external memory interfaces makes it practical to design high-performance imaging systems with balanced computational and memory bandwidths. A framework that allows a developer to choose a microprocessor system that offers the performance and scalability often required by a real-time vision application is presented in [19]. Using the component inspection application as an example, they demonstrate that coprocessor-based solutions, with local memory architects, allow throughput to scale linearly as the number of processors increase. For demanding vision applications, especially those that require future expansion, the most practical solution remains a coprocessor board that is more scalable, has higher throughput, and, ultimately, is cheaper than the native solution.

Finally, one other potential impact is the influx of low-cost universal serial bus (USB) and Firewire cameras into the lucrative consumer market that drives the development of the USB [20] and IEEE 1394 [21] (commonly known as Firewire) communication standards. The USB standard was designed to replace typical parallel and serial I/O ports (such as RS232) and has been widely accepted by the PC industry. USB 2.0 is expected to have a speed over 120-240 MB/s. The IEEE 1394 standard was designed as a high-speed bus with digital video as its target application. The bus currently runs at speeds up to 400 MB/s and expects to exceed 1600 MB/s in the near future.

Prototyping Machine Vision Design

Imaging sensors are characterized by their specific bandwidths, or wavelengths, of light, which maximize the sensor response and will provide it with an optimum operating environment. It is desired that the photo-detector respond only to the light from the illumination source structured for the object but not that of ambient lighting. Synthetic images [22] can efficiently be used to study the effects of illumination and vision system design parameters on image accuracy, providing insight into the accuracy and efficiency of image-processing algorithms in determining part location and orientation for specific applications, as well as reducing the number of hardware prototype configurations to be built and evaluated. Fig. 9 compares the processes used to generate synthetic images for photo-realistic and physically accurate synthetic images for vision system applications.

As shown in Fig. 9, an accurate mathematical model is needed to describe the physical scene and the vision system used to capture that scene. This model is used to simulate scene illumination, which is represented as an array of (pixel) radiances. This array of radiances is then converted to energy/area values, which are transformed by a mapping based on a model of the system sensor and how it converts incident light energy into gray-scale values.

The physically accurate synthetic image is simulated in a two-step process. In the first step, *RADIANCE*, a freely distributed software package from the Lighting Systems Research Group of the Lawrence Berkeley Laboratory, is used to solve the radiative heat transfer equation. In the second step, the sensor model for the computer vision system is modeled using a power law [10].

Fig. 10 quantitatively compares various methods of generating synthetic images, where synthetic images of the retroreflective background were generated and compared to a captured image of the retroreflective field (Fig. 10). As seen in Fig. 10(a), a CAD-generated image assuming an ideal diffuse surface results in an image that is nearly black. Fig. 10(b) illustrates *RADIANCE*'s ability to model the retroreflective background; however, the illuminated area is too small and too sharply defined. Incorporation of the finite aperture [Fig. 10(d)] results in an image with a more acceptable transition between the illuminated and non-illuminated areas, but the illuminated area is still too small. The importance of accurate source-emission distribution modeling is shown in Fig. 10(c). Other detailed illustrative examples of using physically accurate synthetic images can be found in [23].

The benefits of this realistic image synthesis are threefold. First, it provides a rational basis for designing the hardware and software components of a machine vision system. Second, it provides a standard platform for comparing algorithms and predicting the optimal algorithm (and optimal performance) for a specific application. Additionally, it provides an opportunity to perform an in-depth study of the factors that can significantly degrade the performance of image-processing algorithms and aid in the determination of critical design parameters. A third benefit is the ultimate development of a well-designed CAD-tool that utilizes physically accurate synthetic images to accurately and inexpensively predict the performance of a proposed vision system design prior to implementation or construction of a prototype, minimizing the need to build and test a large number of hardware configurations. Such a tool also allows necessary changes in part design to be made earlier in the design phase, significantly reducing implementation time and improving industrial reliability.

Summary

We have presented trends of prototyping design and automation with an emphasis on two subtopics. The first was a brief review of discrete-event and hybrid systems prototyping. A simple software environment system was developed for simulating, analyzing, synthesizing, monitoring, and controlling discrete-event and hybrid systems. The second was a review of the trends in prototyping real-time machine vision system design. Specifically, we presented an alternative system design to overcome problems associated with a traditional video-based vision systems: a physically accurate image synthesis method as a flexible and practical tool for examining a large number of hardware/software configuration combinations for a wide range of parts.

Keywords

Prototyping, discrete event, machine vision, manufacturing, design tool.

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Kok-Meng Lee received his B.S. degree in mechanical engineering from the State University of New York at Buffalo in 1980 and earned his S.M. and Ph.D. degrees in mechanical engineering from MIT in 1982 and 1985, respectively. He is a Woodruff Associate Professor in the George W. Woodruff School of Mechanical Engineering at Georgia Institute of Technology. Dr. Lee's research interests include system dynamics and control, robotics, automation and opto-mechatronics. Dr. Lee has been an active member of the ASME Dynamic System and Control Division and of the IEEE Robotics and Automation Society. He served as an Associate Editor for the *IEEE Transaction on Robotics and Automation* and *IEEE Robotics & Automation Magazine*, and as an Editor for the *IEEE/ASME Transaction on Mechatronics*. He served as a General Chair for the 1999 IEEE/ASME International Conference on Advanced Intelligent Mechatronics. He is a senior member of SME. Other recognition of his contributions includes the National Science Foundation (NSF) Presidential Young Investigator Award, Sigma Xi Junior Faculty Research Award, International Hall of Fame New Technology Award. He holds six patents regarding machine vision, 3-DOF variable reluctance spherical motors, and optical orientation encoders.

Tarek M. Sobh received the B.Sc. in engineering degree with honors in computer science and automatic control from the Faculty of Engineering, Alexandria University, Egypt in 1988, and M.S. and Ph.D. degrees in computer and information science from the School of Engineering, University of Pennsylvania in 1989 and 1991, respectively. He is currently the Dean of the School of Engineering at the University of Bridgeport, Connecticut; the Founding Director of the Interdisciplinary Robotics, Intelligent Sensing, and Control (RISC) laboratory; a Professor of Computer Science, Computer Engineering, Electrical and Mechanical Engineering; and the Chairman of the Prototyping Technical Committee of the IEEE Robotics and Automation Society. He was the Interim Chairman of Computer Science and Computer Engineering and the Director of External Engineering Programs at the University of Bridgeport. He was an Associate Professor of Computer Science and Computer Engineering at the University of Bridgeport from 1995-1999; a Research Assistant Professor of Computer Science at the Department of Computer Science, University of Utah from 1992-1995; and a Research Fellow at the General Robotics and Active Sensory Perception (GRASP) Laboratory of the University of Pennsylvania from 1989-1991. He was the Chairman of the Discrete-Event and Hybrid Systems Technical Committee of the IEEE Robotics and Automation Society from 1992-1999. His background is in the fields of computer science and engineering, control theory, robotics, automation, manufacturing, AI, computer vision, and signal processing. He has published over 90 journal and conference papers and book chapters in these and other areas. Dr. Sobh has been awarded many grants to pursue his research. He is a Licensed Professional Electrical Engineer (P.E.) and a Certified Manufacturing Engineer (CMfgE) by the Society of Manufacturing Engineers, a member of Tau Beta Pi (the Engineering Honor Society), Sigma Xi (the Scientific Research Society), Phi Beta Delta (the International Honor Society), and Upsilon Pi Epsilon (the Computing Honor Society). Dr. Sobh was the recipient of the Best Paper Award at the World Automation Congress Conference (WAC 98) in Anchorage, Alaska.

Addresses for correspondence: Kok-Meng Lee, George W. Woodruff School of Mechanical Engineering, Georgia Institute of Technology, Atlanta, GA 30332-0405. Tel: +1 404 894 7402. Fax: +1 404 894 9342. E-mail: kokmeng.lee@me.gatech.edu